CCD ADAPTIVE FILTERING TECHNIQUES WITH APPLICATIONS TO COMMUNICATION SYSTEMS

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ABSTRACT

Some CCD variable or adaptive filtering techniques are described, in particular to perform multiple filtering, band-pass analysis, multifrequency-tone receiving and adaptive equalization. A special algorithm is presented in order to compensate for the charge transfer loss which is typical of CCD devices. A structure is in detail described, based on the use of a CCD tapped-delay-line and a microprocessor evaluating the filter coefficients and driving a set of MDAC converters. The implementation of a CCD multifrequency-tone receiver using this structure is shown and experimental results are reported.

INTRODUCTION

Variable or adaptive filtering techniques are of high interest in communication systems, in particular to perform multiple filtering, band-pass analysis, multifrequency-tone receiving and adaptive equalization. Charge-coupled-device (CCD) filtering structures have been proposed or used up to now for the above applications, mainly to perform fixed filtering operations.

In this paper we present some structures and procedures permitting to perform the required variable or adaptive filtering (in particular multiple filterings), combining the advantage of low-cost, high-integration CCD implementation with the variability and adaptivity of the filtering operation. A structure is in detail presented, based on the use of a CCD tapped-delay-line and a microprocessor, evaluating the filter coefficients and driving a set of multiplying digital-to-analog converters (MDAC). Procedures increasing the adaptation speed and the filtering efficiency (compensating for the charge-transfer loss effects) are described. As a typical example, the project and implementation of a CCD multifrequency-tone receiver is presented with experimental results.

SOME CCD VARIABLE OR ADAPTIVE FILTERING STRUCTURES

A first approach to perform variable filtering with CCD structures corresponds to use suitable memories, in which the set of coefficients of the desired filterings are maintained for the necessary time and switched subsequently to the analog multipliers at different CCD taps. The memories can be of analog type, using for instance precise potentiometers or CCD recirculating memories (in this last case a regenerative procedure must be provided to compensate for the charge transfer loss in CCD delay lines). Digital memories can be also used with the addition of digital-to-analog converters driving the analog multipliers or MDAC circuits (1). A general block-diagram for all the above considered structures is shown in Fig. 1.

![Fig. 1 - General block-diagram of variable CCD filtering structures using memories to store the set of coefficients for the desired filterings.](image)

Another attractive solution to perform multiple filtering or band-pass analysis by using a CCD tapped-delay-line corresponds to make use of a single set of filter coefficients and of a suitable frequency shift procedure of the sampled signal spectrum. Indeed it is well known that a frequency shift of the sampled signal spectrum equal to half the sampling frequency can be obtained by multiplying the signal samples \( s_n \) by \((-1)^n\) \((2) (3)\).

By assuming a sampling frequency \( f_s = 2f_M \), \( f_M \) being the maximum signal frequency, the low-pass filtering of the original signal samples gives the signal components \( 0 \) to \( f_M/2 \), while the low-pass filtering of the \((-1)^n s_n \) samples gives the signal components \( f_M/2 \) to \( f_M \) (of course frequency-translated and inverted into the low frequency band \( 0 \) to \( f_M/2 \)). The described operation can be implemen-
tated by two different structures. The first one is shown in Fig. 2 (a), where a unique hardware filtering unit processes the original signal and its frequency-translated version (after recirculation through the CCD delay line of sufficient length) in subsequent times. This structure does not allow to operate continuously on the input signal and can be suitable for applications where such an intermittent operation is accepted. One possible example is the multifrequency-tone communication, where at certain time intervals the receiver has to determine whether a tone is present either in a low or in a high frequency band. A second solution, suitable for continuous operation of the input signal, is shown in Fig. 2 (b). In this case the same set of filter coefficients is supplied to two hardware filtering units (CCD tapped delay lines): the first one low-pass filters the input signal and gives at the output the low-frequency signal components; the second one low-pass filters the \((-1)^n h_n\) and gives at the output, after another multiplication by \((-1)^n\) (frequency translation of \(f_{1/2}\)), the high frequency signal components.

As described for digital filtering structures and implemented in practice constructing hardware band-pass analyzers (3), the above multiple filtering procedure shown in Fig. 2 can be easily repeated to obtain a band-pass analysis extending over all the signal spectrum.

Another solution to perform CCD variable or adaptive filtering in a flexible and efficient way, a part the cost, is represented by the use of a microprocessor evaluating the filter coefficients and driving a set of MDAC converters (Fig. 3). In this structure the filter transfer function can be easily digitally controlled by the microprocessor through the above coupling circuits and of course adaptation algorithms of the filter coefficients can be implemented more immediately (for instance the clipped Widrow LMS algorithm) (4).

![Fig. 3 - Block-diagram of a CCD variable or adaptive filtering structure, using a microprocessor evaluating the filter coefficients and driving a set of MDAC converters.](image)

In all the above CCD structures, to increase the filter efficiency, a compensation of the charge transfer loss is highly desirable. A simple and efficient coefficient modification algorithm was recently defined to this purpose (5): if \(h_k\) are the usual filter coefficients, the modified coefficients \(h'_k\) are obtained from the nominal ones \(h_k\) through the recursive relation

\[
h'_k = \frac{1}{1-k \varepsilon} \left[ h_k + \varepsilon (k-1) h'_{k-1} \right]
\]

where \(\varepsilon\) is the value of the charge-transfer inefficiency. Fig. 4 shows an example of application of the above technique to the implementation of a low-pass filter (in particular Fig. 4 (b): the reduction of amplitude error from the uncompensated implementation (solid line) to compensated one (dotted line), which is practically reducing to zero.

An example of practical implementation of the last CCD structure with microprocessor, using the

![Fig. 2 - CCD tapped-delay-line structures for multiple filtering, using a single set of filter coefficients: (a) with intermittent operation on the input signal; (b) with continuous operation.](image)
it follows easily from (2) that
\[ |c(t)|^2 = x(t)^2 + x(t)^2 = a^2 \tag{4} \]

Suppose now that we receive a signal \( s(t) \) which may contain one of \( M \) possible tones having equal amplitude at the angular frequencies \( \omega_i \), \( i = 1, \ldots, M \). In other words let us suppose to receive a signal of the form
\[ s(t) = \sum s_i(t \omega_i + \varphi_i) + n(t) \tag{5} \]
if the tone is present, or of the form \( s(t) = n(t) \) if no tone is transmitted, being \( n(t) \) the contribution of any type of noise, interference, disturbances, that may be received superimposed on the transmitted signal.

Let us consider now \( M \) couples of band-pass quadrature filters \( H_i(\omega) \) and \( \tilde{H}_i(\omega) \), \( i = 1, \ldots, M \). The filters \( H_i(\omega) \) are non overlapping band-pass filters having \( \omega_i \) as respective center frequencies. The filters \( \tilde{H}_i(\omega) \) are defined as
\[ \tilde{H}_i(\omega) = \frac{-j}{\omega - \omega_i} \tag{6} \]
Therefore \( H_i(\omega) \) and \( \tilde{H}_i(\omega) \) represent two band-pass quadrature filters and the output of \( H_i(\omega) \) is the Hilbert transform of the output of \( \tilde{H}_i(\omega) \). Denoting these two outputs \( x_i(t) \) and \( \tilde{x}_i(t) \) respectively and neglecting for the moment the contribution of \( n(t) \) in (5) we have that either
\[ x_i^2(t) + \tilde{x}_i^2(t) = a^2 \tag{7} \]
if the \( i \)-th tone transmitted was \( \omega_i \), or
\[ x_i^2(t) + \tilde{x}_i^2(t) = 0 \tag{8} \]
if a different tone or no tone was transmitted.

Hence the receiver algorithm can be stated as follows:
- a) determine the \( M \) envelope squares \[ |c_i|^2 \]
\[ |c_i|^2 = x_i^2(t) + \tilde{x}_i^2(t) \quad i = 1, \ldots, M \tag{9} \]
- b) choose the maximum of the \( M \) envelope squares
\[ C = \max \{ |c_i|^2 \} \tag{10} \]
which will coincide with one of the \( M \) \[ |c_i|^2 \] as
\[ C = |c_j|^2 \tag{11} \]
c) if \( C > T \), the \( j \)-th tone is detected, if \( C < T \), no transmitted tone is assumed, where \( T \) is a specified threshold that depends on the allowable probability of false alarm \( P_F \) (i.e. the probability of detecting a tone when no tone was actually transmitted).

In the ref. (6) it is shown that, in presence of white Gaussian noise, the threshold \( T \) may be chosen to be
\[ N = 2 \log_{10} \left( \frac{P}{f_f} \right) \]  

where \( N \) is the noise power in the filter pass-bands (all supposed of equal bandwidth). In ref. (6), the probability of correct detection is also shown in terms of the receiver threshold \( T \) and the signal-to-noise ratio of the received signal.

A final consideration is to be done for the implementation of the filters \( H_1(\omega) \) and \( H_2(\omega) \). If we choose to implement them in discrete form, it is more convenient to consider the FIR structure instead of the IIR one. In fact, the first one requires the computation of the envelope squares. On the contrary, the IIR structure requires the computation of all the output samples for every filter. Therefore, the FIR solution allows the multiplexing of a definite hardware structure to the different filters to be implemented with considerable saving in system complexity and cost.

Implementation of the Receiver

The set of filters employed for frequency discrimination was implemented by means of a CTD tapped delay line. The resulting structure is a programmable filter in which the coefficient weights drive OPAC converters: this configuration makes possible to control the filter transfer function by means of a digital circuitry.

As it is shown in Fig. 5, a microprocessor has the task to control the filter coefficients. The final structure results little more complicated than a programmable filter, due to the fact that two sets of coefficients for the couple of band-pass quadrature filters of a given tone must be switched in a very short time (i.e., during the time interval between two sampling instants). In fact, the output values of the two filters must be obtained starting from the same configuration of signal samples within the CTD delay line, as it is clear from eq. (9). Therefore, it is necessary to employ the external set of latches so that the microprocessor can provide the coefficients of both filters and, subsequently, only a handshake signal is required for the switching of the two sets of coefficients.

The coefficient weights of the set of filters can further be computed in advance and stored in a suitable table so that the microprocessor can give the coefficients simply by transferring the values from the internal table to the output circuitry. Coefficient weights have been computed by means of the well known window method (3) and modified by the algorithm (1) in order to compensate for the charge transfer loss.

The receiver structure, implemented according to Fig. 5, uses a single 32 cell CTD delay line, time-multiplexed among all the filters. The sampling frequency is 8 KHz and \( M = 4 \) tones were considered in the tests.

![Fig. 5 - Block-diagram of the implemented multifrequency-tone receiver.](image)

![Fig. 6 - Example of an implemented band-pass Hilbert transformer \((f_c=1.5 \text{ KHz}), M=0.5 \text{ KHz}\).](image)

An example of an implemented band-pass Hilbert transformer having a center frequency \( f_c = 1.5 \text{ KHz} \) and a bandwidth (at -6 db) \( M = 0.5 \text{ KHz} \) is shown in Fig. 6.

REFERENCES